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ABSTRACT OF THE DISCLOSURE

A multi-layer capacitor device includes a capacitor body including ~~top and bottom surfaces and opposed side surfaces which have continuously flat surfaces and are disposed between the top and bottom surfaces and opposed end surfaces disposed between the top and bottom surfaces and the opposed side surfaces.~~ The capacitor body includes a plurality of first electrode plates and a plurality of second electrode plates. The first and second electrode plates are interleaved with each other in opposed and spaced apart relation. A dielectric material is located between each opposed set of the first and second electrode plates. The first and second electrode plates each include a main electrode portion and a plurality of spaced apart lead structures extending therefrom. Respective lead structures of the first electrodes plates are located adjacent respective lead structures of the second electrode plates in an interdigitated arrangement. A plurality of electrical terminals are located on each of the opposed side surfaces of the capacitor body, ~~corresponding lead structures of the first electrode plates and corresponding lead structures of the second electrode plates being electrically connected together by respective ones of the electrical terminals to define a~~ A plurality of first polarity electrical terminals and a plurality of second polarity electrical terminals, respectively, located on the capacitor body. Each of the first polarity terminals is disposed opposite to another of the first polarity terminals across the capacitor body and each of the second polarity terminals is disposed opposite to another of the second polarity terminals across the capacitor body. Each of the lead structures of the first and second electrode plates have a length L and a width W and a ratio L/W is equal to about 3 or less, and preferably between 0.4 and 1.3.